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TITLE: Reset circuit of electronic device

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PATENT-FAMILY:

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BASIC-ABSTRACT:

NOVELTY - A reset circuit for an electronic device is provided to stabilize the output of the reset circuit operating at a low voltage by installing a pull-down resistor and a pull-up resistor at the output part of the reset circuit.

DETAILED DESCRIPTION - In a reset circuit for an electronic device, a pull-down resistor(R41) of 100kohm is installed between the reset output terminal of a MAX811/812 IC(30) and a pull-up resistor(R42) is installed between the output terminal and the VCC. Thus, A reset signal by a reset switch(SW) turns on an

NMOS transistor(N30) and produces a low potential reset signal. When the switch is off, a PMOS transistor(P30) is turned on and produces a high potential reset signal. But when the VCC is below 1V, the NMOS and PMOS transistors(N30,P30) do not turn on normally. So to maintain a stable low potential, the VCC is divided by the pull-down and pull-up resistors(R41,R42).

CHOSEN-DRAWING: Dwg.1/10

TITLE-TERMS: RESET CIRCUIT ELECTRONIC DEVICE

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